an interface disposed on a surface of the housing to enable

communication between the device and the notebook computer when

the device is docked;

a memory to store an operating system; and

a processor to operate as a system processor of the notebook computer

when the device is docked and to operate as a system processor of

the device when the device is undocked.

2.   The electronic device of claim 1, further comprising an input controller to

receive input data into the device when the device is undocked.

3.   The electronic device of claim 2, further comprising core memory to store the

input data when the device is undocked.

4.   The electronic device of claim 3, further comprising an output controller to

provide output data from the device when the device is undocked.

5.   The electronic device of claim 4, further comprising a visual display disposed

on a surface of the housing, the visual display being coupled to the input

controller to provide the input data via pen-based entries on the display and

being coupled to the output controller to provide the output data via the

display.

6. (Amended) The electronic device of claim 1, wherein the memory is a core memory to store a mini operating system.

7. The electronic device of claim 1, further comprising a battery to provide power to the processor when the electronic device is undocked.

8. The electronic device of claim 7, wherein the interface is coupled to the battery to charge the battery when the electronic device is docked.

9. The electronic device of claim 8, wherein the notebook computer is to provide power to the processor when the electronic device is docked.

10. The electronic device of claim 9, wherein the processor is to operate at a higher frequency and at a higher voltage when the device is docked than when the device is undocked.

11. The electronic device of claim 1, wherein the processor is to operate at a higher frequency and at a higher voltage when the device is docked than when the device is undocked.

12. (Amended) A base computer comprising:

    a docking port to receive a hand-held core computer having a processor to operate as a system processor of the base computer when the device is docked and to operate as a system processor of the core

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computer when the device is undocked, the hand-held core including a

memory to store an operating system;

an interface in the docking port to enable communication between the

core computer and the base computer when the core computer is

docked; and

a memory to store an operating system.

13.   (Amended) The base computer of claim 12, wherein the memory of the base

computer to store a full operating system, the memory of the hand-held core

to store a mini operating system.

14.   The base computer of claim 12, wherein the interface is to couple a power

supply of the base computer to a battery in the core computer to charge the

battery and to provide power to the processor when the core computer is

docked.

15.   The base computer of claim 14, wherein the processor is to operate at a

higher frequency and at a higher voltage when the processor operates as a

system processor of the base computer than when the processor operates as

a system processor of the core computer.

16.   The base computer of claim 12, wherein the processor is to operate in one of

a high power mode and a low power mode according to user preference.

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17. (Amended) A method of operating a computer system comprising:

operating a processor as a system processor of a notebook computer when a core computer is docked in a docking port of the notebook computer, the notebook computer including a memory to store an operating system; and

operating the processor as a system processor of the core computer when the core computer is undocked, the core computer including a memory to store an operating system.

18. The method of claim 17, further comprising synchronizing memory of the notebook computer with memory of the core computer when the core computer is docked.

19. The method of claim 17, further comprising charging a battery in the core computer when the core computer is docked.

20. The method of claim 17, wherein operating the processor as a system processor of the notebook computer includes operating the processor at a higher frequency and voltage than when operating the processor as a system processor of the core computer.

21. The method of claim 17, wherein operating the processor as a system processor of the notebook computer includes running a full operating system

on the processor, and operating the processor as a system processor of the core computer includes running a mini operating system on the processor.